

General Description

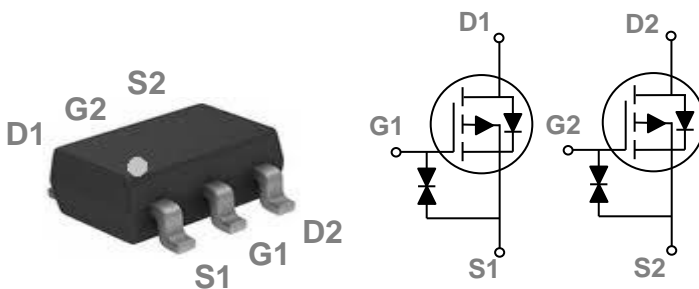
These dual P Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

BVDSS	RDSON	ID
-20V	600mΩ	-540mA

Features

- Fast switching
- Green Device Available
- Suit for 1.5V Gate Drive Applications

SOT363 Dual Pin Configuration



Applications

- Notebook
- Load Switch
- Networking
- Hand-held Instruments

Absolute Maximum Ratings Tc=25°C unless otherwise noted

Symbol	Parameter	Rating	Units
V _{DS}	Drain-Source Voltage	-20	V
V _{GS}	Gate-Source Voltage	±8	V
I _D	Drain Current – Continuous (T _A =25°C)	-540	mA
	Drain Current – Continuous (T _A =70°C)	-430	mA
I _{DM}	Drain Current – Pulsed ¹	-2.16	A
P _D	Power Dissipation (T _A =25°C)	278	mW
	Power Dissipation – Derate above 25°C	2.2	mW/°C
T _{STG}	Storage Temperature Range	-55 to 150	°C
T _J	Operating Junction Temperature Range	-55 to 150	°C

Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
R _{θJA}	Thermal Resistance Junction to ambient	---	450	°C/W

Electrical Characteristics (T_J=25 °C, unless otherwise noted)
Off Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =-250uA	-20	---	---	V
ΔBV _{DSS} /ΔT _J	BV _{DSS} Temperature Coefficient	Reference to 25°C, I _D =-1mA	---	-0.01	---	V/°C
I _{DSS}	Drain-Source Leakage Current	V _{DS} =-20V, V _{GS} =0V, T _J =25°C	---	---	-1	uA
		V _{DS} =-16V, V _{GS} =0V, T _J =125°C	---	---	-10	uA
I _{GSS}	Gate-Source Leakage Current	V _{GS} =±8V, V _{DS} =0V	---	---	±20	uA

On Characteristics

R _{DS(ON)}	Static Drain-Source On-Resistance	V _{GS} =-4.5V, I _D =-0.3A	---	440	600	mΩ
		V _{GS} =-2.5V, I _D =-0.2A	---	610	850	
		V _{GS} =-1.8V, I _D =-0.1A	---	810	1200	
		V _{GS} =-1.5V, I _D =-0.1A	---	1020	1600	
		V _{GS} =-1.2V, I _D =-0.1A	---	1800	3000	
V _{GS(th)}	Gate Threshold Voltage	V _{GS} =V _{DS} , I _D =-250uA	-0.3	-0.6	-1.0	V
ΔV _{GS(th)}	V _{GS(th)} Temperature Coefficient		---	3	---	mV/°C

Dynamic and switching Characteristics

Q _g	Total Gate Charge ^{2, 3}	V _{DS} =-10V, V _{GS} =-4.5V, I _D =-0.2A	---	1	2	nC
Q _{gs}	Gate-Source Charge ^{2, 3}		---	0.28	0.5	
Q _{gd}	Gate-Drain Charge ^{2, 3}		---	0.18	0.4	
T _{d(on)}	Turn-On Delay Time ^{2, 3}	V _{DD} =-10V, V _{GS} =-4.5V, R _G =10Ω I _D =-0.2A	---	8	16	ns
T _r	Rise Time ^{2, 3}		---	5.2	10	
T _{d(off)}	Turn-Off Delay Time ^{2, 3}		---	30	60	
T _f	Fall Time ^{2, 3}		---	18	36	
C _{iss}	Input Capacitance	V _{DS} =-10V, V _{GS} =0V, F=1MHz	---	40	78	pF
C _{oss}	Output Capacitance		---	15	30	
C _{rss}	Reverse Transfer Capacitance		---	6.5	13	

Drain-Source Diode Characteristics and Maximum Ratings

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I _S	Continuous Source Current	V _G =V _D =0V, Force Current	---	---	-0.54	A
I _{SM}	Pulsed Source Current		---	---	-1.08	A
V _{SD}	Diode Forward Voltage	V _{GS} =0V, I _S =-0.2A, T _J =25°C	---	---	-1	V

Note :

1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2. The data tested by pulsed, pulse width ≤ 300us, duty cycle ≤ 2%.
3. Essentially independent of operating temperature.

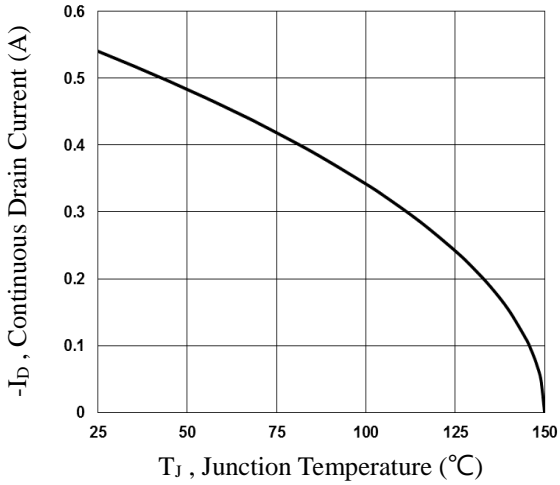


Fig.7 Continuous Drain Current vs. T_c

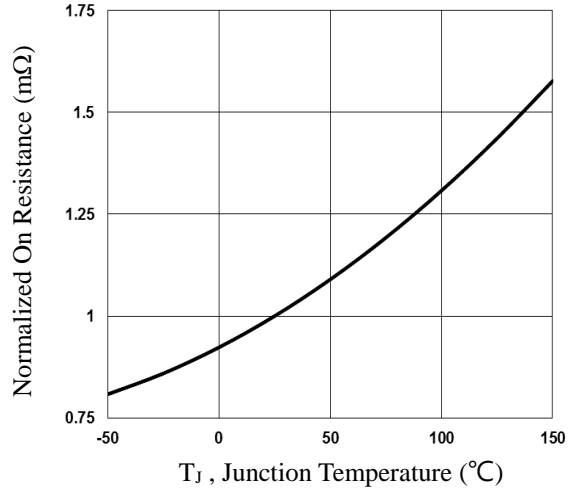


Fig.8 Normalized $R_{DS(on)}$ vs. T_J

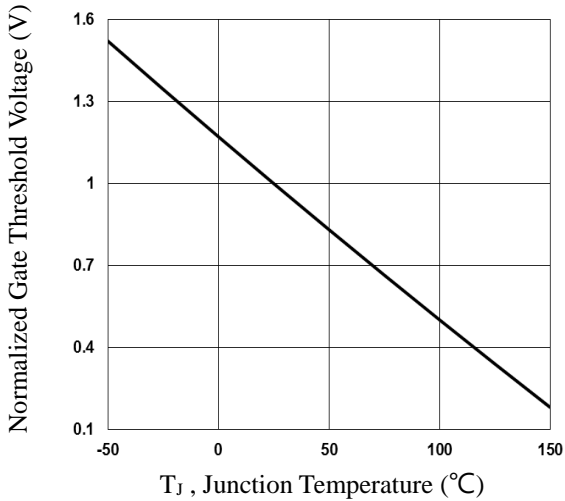


Fig.9 Normalized V_{th} vs. T_J

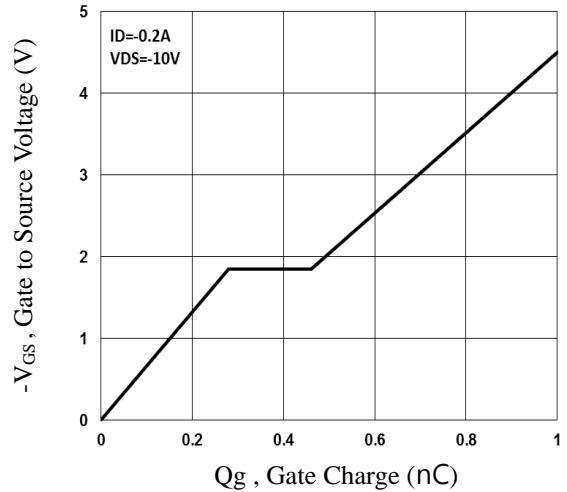


Fig.10 Gate Charge Waveform

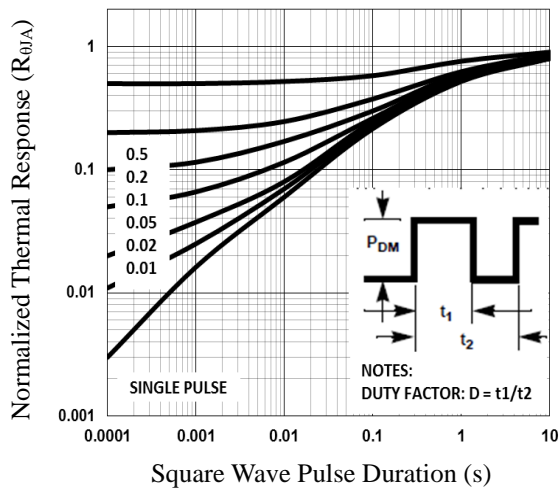


Fig.11 Normalized Transient Impedance

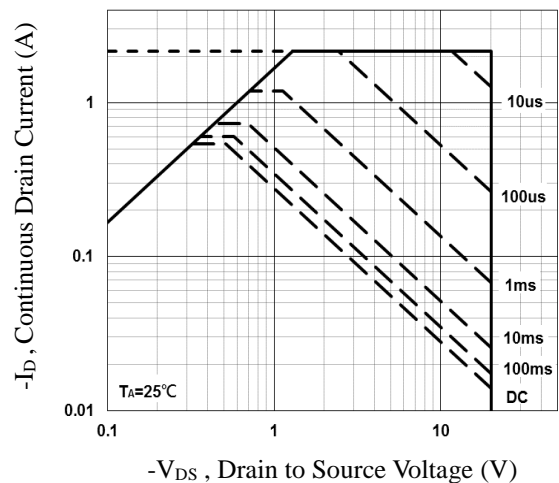
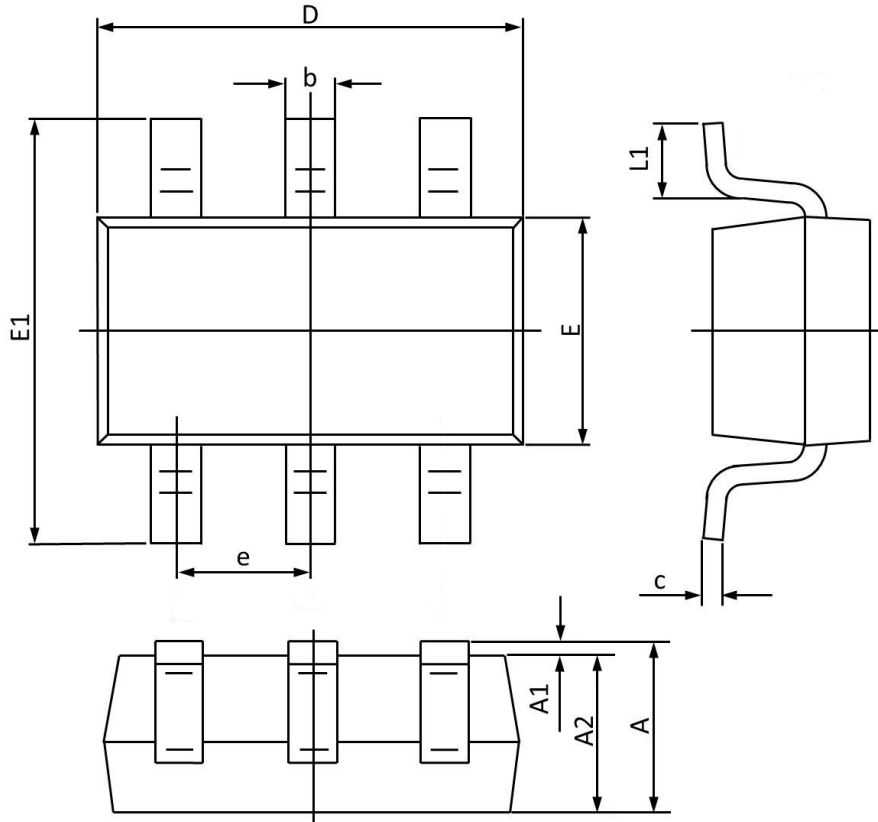


Fig.12 Maximum Safe Operation Area

SOT363 Dual PACKAGE INFORMATION



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	MAX	MIN	MAX	MIN
A	1.100	0.800	0.043	0.031
A1	0.100	0.000	0.004	0.000
A2	1.000	0.800	0.039	0.031
b	0.330	0.100	0.013	0.004
c	0.250	0.100	0.010	0.004
D	2.200	1.800	0.087	0.071
E	1.350	1.150	0.053	0.045
E1	2.400	1.800	0.094	0.071
e	0.65BSC		0.026BSC	
L1	0.350	0.100	0.014	0.004