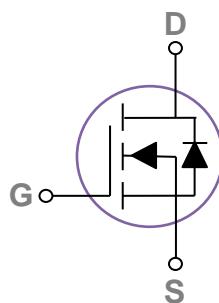


General Description

These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

TO251 Pin Configuration



BVDSS	RDSON	ID
80V	100mΩ	12A

Features

- 80V, 12A, RDS(ON)=100mΩ@VGS = 10V
- Improved dv/dt capability
- Fast switching
- 100% EAS Guaranteed
- Green Device Available

Applications

- Motor Drive
- Power Tools
- LED Lighting

Absolute Maximum Ratings $T_c=25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Rating	Units
V _{Ds}	Drain-Source Voltage	80	V
V _{Gs}	Gate-Source Voltage	±20	V
I _D	Drain Current – Continuous ($T_c=25^\circ\text{C}$)	12	A
	Drain Current – Continuous ($T_c=100^\circ\text{C}$)	7.6	A
I _{DM}	Drain Current – Pulsed ¹	48	A
P _D	Power Dissipation ($T_c=25^\circ\text{C}$)	35	W
	Power Dissipation – Derate above 25°C	0.28	W/°C
T _{STG}	Storage Temperature Range	-55 to 150	°C
T _J	Operating Junction Temperature Range	-55 to 150	°C

Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
R _{θJA}	Thermal Resistance Junction to ambient	---	62	°C/W
R _{θJC}	Thermal Resistance Junction to Case	---	3.6	°C/W

Electrical Characteristics ($T_J=25^\circ\text{C}$, unless otherwise noted)
Off Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$, $I_{\text{D}}=250\mu\text{A}$	80	---	---	V
$\Delta \text{BV}_{\text{DSS}}/\Delta T_J$	BV_{DSS} Temperature Coefficient	Reference to 25°C , $I_{\text{D}}=1\text{mA}$	---	0.09	---	$\text{V}/^\circ\text{C}$
$I_{\text{DS}}^{\text{SS}}$	Drain-Source Leakage Current	$V_{\text{DS}}=80\text{V}$, $V_{\text{GS}}=0\text{V}$, $T_J=25^\circ\text{C}$	---	---	1	μA
		$V_{\text{DS}}=64\text{V}$, $V_{\text{GS}}=0\text{V}$, $T_J=125^\circ\text{C}$	---	---	10	μA
I_{GSS}	Gate-Source Leakage Current	$V_{\text{GS}}=\pm 20\text{V}$, $V_{\text{DS}}=0\text{V}$	---	---	± 100	nA

On Characteristics

$R_{\text{DS(ON)}}$	Static Drain-Source On-Resistance	$V_{\text{GS}}=10\text{V}$, $I_{\text{D}}=10\text{A}$	---	77	100	$\text{m}\Omega$
		$V_{\text{GS}}=4.5\text{V}$, $I_{\text{D}}=8\text{A}$	---	80	115	$\text{m}\Omega$
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{\text{GS}}=V_{\text{DS}}$, $I_{\text{D}}=250\mu\text{A}$	1.2	1.8	2.5	V
			---	-4.4	---	$\text{mV}/^\circ\text{C}$
g_{fs}	Forward Transconductance	$V_{\text{DS}}=10\text{V}$, $I_{\text{D}}=10\text{A}$	---	7	---	S

Dynamic and switching Characteristics

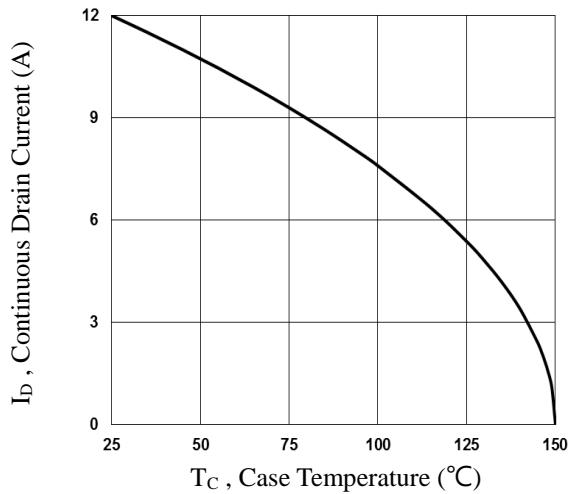
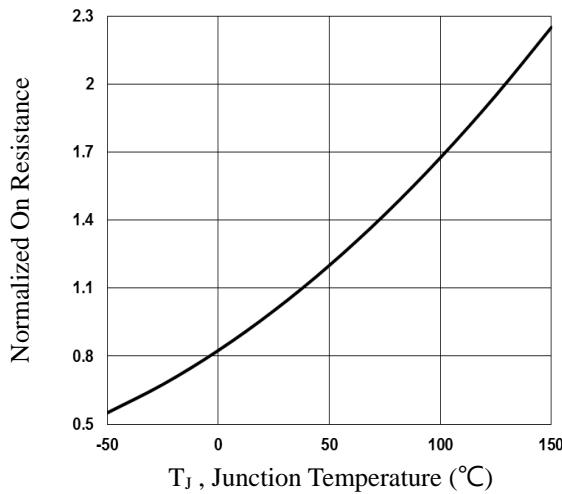
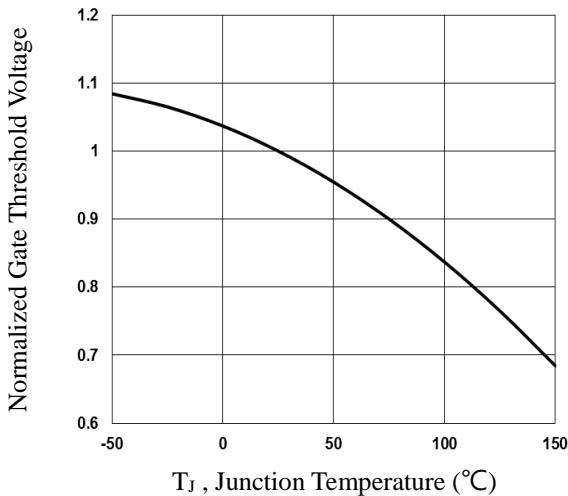
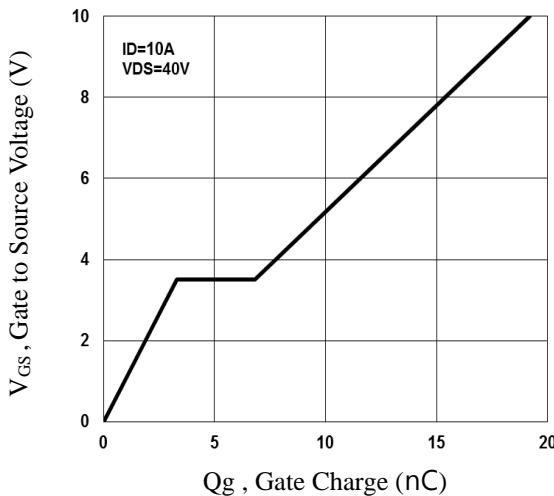
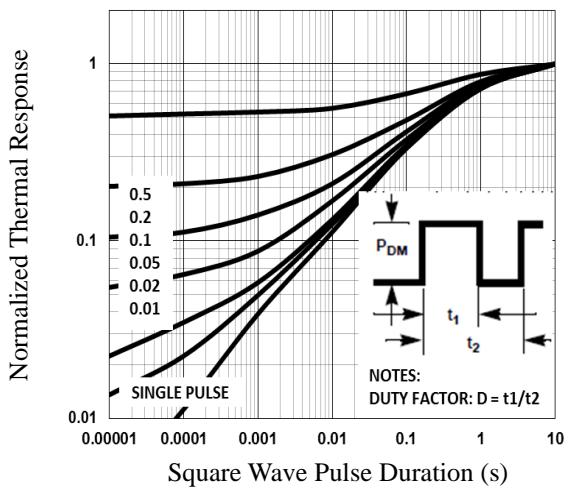
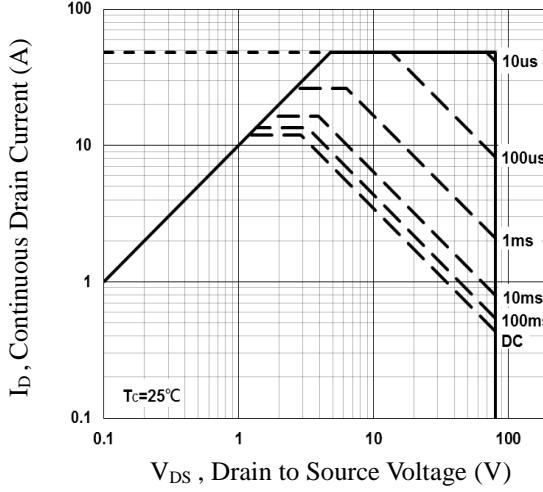
Q_g	Total Gate Charge ^{2, 3}	$V_{\text{DS}}=40\text{V}$, $V_{\text{GS}}=10\text{V}$, $I_{\text{D}}=10\text{A}$	---	19.2	29	nC
Q_{gs}	Gate-Source Charge ^{2, 3}		---	3.3	6	
Q_{gd}	Gate-Drain Charge ^{2, 3}		---	3.5	7	
$T_{\text{d(on)}}$	Turn-On Delay Time ^{2, 3}	$V_{\text{DD}}=30\text{V}$, $V_{\text{GS}}=10\text{V}$, $R_{\text{G}}=6\Omega$	---	5	10	ns
T_r	Rise Time ^{2, 3}		---	25	50	
$T_{\text{d(off)}}$	Turn-Off Delay Time ^{2, 3}		---	22	44	
T_f	Fall Time ^{2, 3}		---	8	16	
C_{iss}	Input Capacitance	$V_{\text{DS}}=30\text{V}$, $V_{\text{GS}}=0\text{V}$, $F=1\text{MHz}$	---	1070	1550	pF
C_{oss}	Output Capacitance		---	40	60	
C_{rss}	Reverse Transfer Capacitance		---	35	50	
R_g	Gate resistance	$V_{\text{GS}}=0\text{V}$, $V_{\text{DS}}=0\text{V}$, $F=1\text{MHz}$	---	1	2	Ω

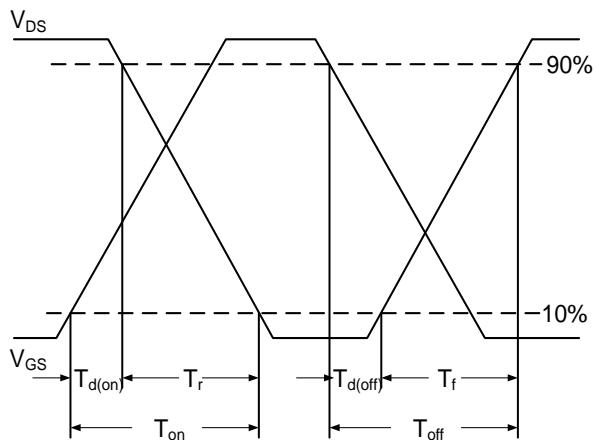
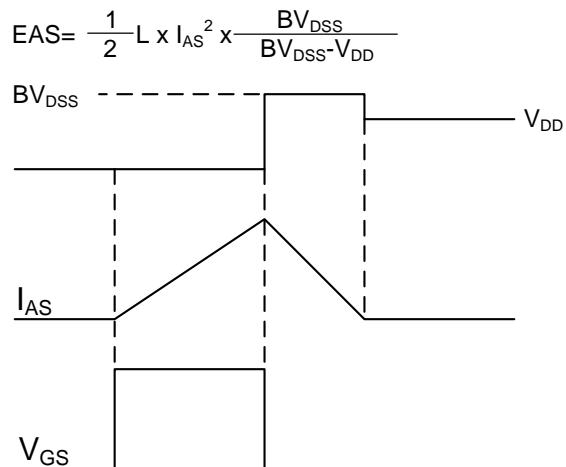
Drain-Source Diode Characteristics and Maximum Ratings

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_s	Continuous Source Current	$V_G=V_D=0\text{V}$, Force Current	---	---	12	A
I_{SM}	Pulsed Source Current		---	---	24	A
V_{SD}	Diode Forward Voltage	$V_{\text{GS}}=0\text{V}$, $I_{\text{s}}=1\text{A}$, $T_J=25^\circ\text{C}$	---	---	1	V

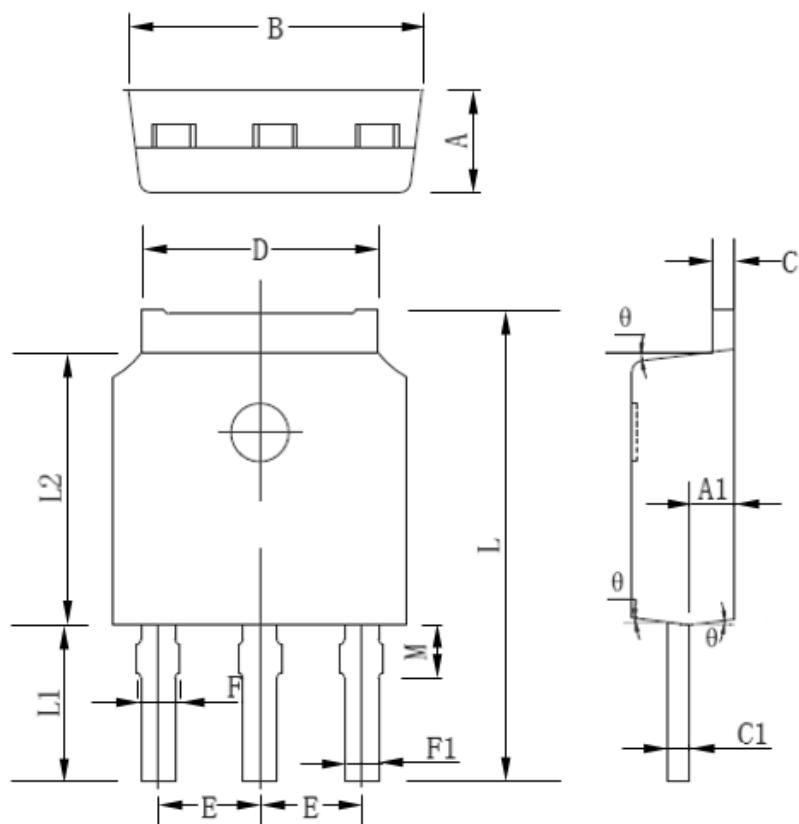
Note :

1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2. The data tested by pulsed, pulse width $\leq 300\text{us}$, duty cycle $\leq 2\%$.
3. Essentially independent of operating temperature.


Fig.1 Continuous Drain Current vs. T_C

Fig.2 Normalized RD_{SON} vs. T_J

Fig.3 Normalized V_{th} vs. T_J

Fig.4 Gate Charge Waveform

Fig.5 Normalized Transient Impedance

Fig.6 Maximum Safe Operation Area


Fig.7 Switching Time Waveform

Fig.8 EAS Waveform

TO251 PACKAGE INFORMATION



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	2.200	2.400	0.087	0.094
A1	0.910	1.110	0.036	0.044
B	6.500	6.700	0.256	0.264
C	0.460	0.580	0.018	0.023
C1	0.460	0.580	0.018	0.023
D	5.120	5.520	0.202	0.217
E	2.186	2.386	0.086	0.094
F	0.780	1.020	0.030	0.040
F1	0.660	0.860	0.026	0.034
L	10.200	10.800	0.402	0.425
L1	3.200	3.800	0.126	0.150
L2	6.000	6.200	0.236	0.244
M	1.000	1.200	0.039	0.047
θ	3°	9°	3°	9°