

### General Description

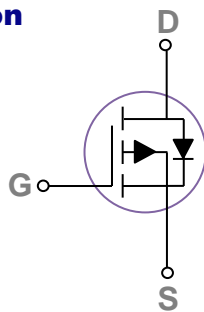
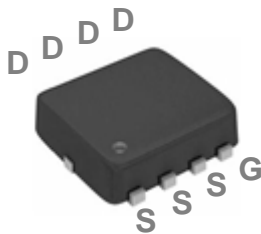
These P-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

BVDSS	RDSON	ID
-40V	14mΩ	-38A

### Features

- -40V,-38A,  $R_{DS(ON)} = 14m\Omega @ V_{GS} = -10V$
- AEC-Q101 qualified
- Fast switching
- Green Device Available
- Suit for -4.5V Gate Drive Applications

### PPAK3x3 Pin Configuration



### Applications

- MB / VGA / Vcore
- POL Applications
- Load Switch
- LED Application

### Absolute Maximum Ratings $T_c=25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	-40	V
$V_{GS}$	Gate-Source Voltage	$\pm 20$	V
$I_D$	Drain Current – Continuous ( $T_c=25^\circ\text{C}$ )	-38	A
	Drain Current – Continuous ( $T_c=100^\circ\text{C}$ )	-24	A
$I_{DM}$	Drain Current – Pulsed <sup>1</sup>	-152	A
EAS	Single Pulse Avalanche Energy <sup>2</sup>	130	mJ
IAS	Single Pulse Avalanche Current <sup>2</sup>	-51	A
$P_D$	Power Dissipation ( $T_c=25^\circ\text{C}$ )	52	W
	Power Dissipation – Derate above $25^\circ\text{C}$	0.42	W/°C
$T_{STG}$	Storage Temperature Range	-55 to 150	°C
$T_J$	Operating Junction Temperature Range	-55 to 150	°C

### Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction to ambient	---	62	°C/W
$R_{\theta JC}$	Thermal Resistance Junction to Case	---	2.4	°C/W

**Electrical Characteristics (T<sub>J</sub>=25 °C, unless otherwise noted)**
**Off Characteristics**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> =-250uA	-40	---	---	V
I <sub>DSS</sub>	Drain-Source Leakage Current	V <sub>DS</sub> =-40V, V <sub>GS</sub> =0V, T <sub>J</sub> =25°C	---	---	-1	uA
		V <sub>DS</sub> =-32V, V <sub>GS</sub> =0V, T <sub>J</sub> =125°C	---	---	-10	uA
I <sub>GSS</sub>	Gate-Source Leakage Current	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V	---	---	±100	nA

**On Characteristics**

R <sub>DS(ON)</sub>	Static Drain-Source On-Resistance	V <sub>GS</sub> =-10V, I <sub>D</sub> =-15A	---	11.3	14	mΩ
		V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-8A	---	15.6	21	mΩ
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>GS</sub> =V <sub>DS</sub> , I <sub>D</sub> =-250uA	-1.0	-1.6	-2.5	V
g <sub>fs</sub>	Forward Transconductance	V <sub>DS</sub> =-10V, I <sub>D</sub> =-4A	---	11	---	S

**Dynamic and switching Characteristics**

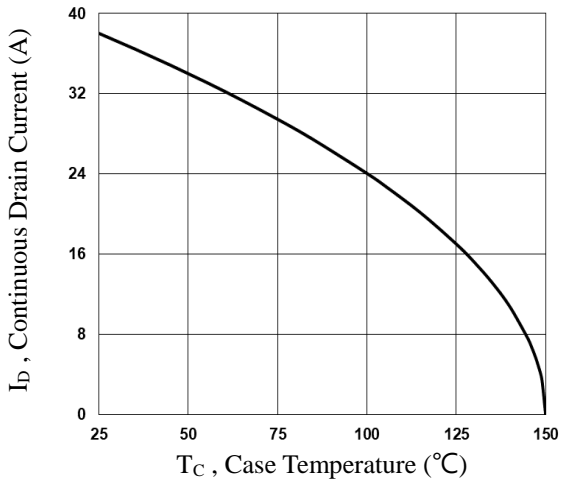
Q <sub>g</sub>	Total Gate Charge <sup>3, 4</sup>	V <sub>DS</sub> =-32V, V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-10A	---	22.2	40	nC
Q <sub>gs</sub>	Gate-Source Charge <sup>3, 4</sup>		---	8.2	16	
Q <sub>gd</sub>	Gate-Drain Charge <sup>3, 4</sup>		---	8.8	16	
T <sub>d(on)</sub>	Turn-On Delay Time <sup>3, 4</sup>	V <sub>DD</sub> =-20V, V <sub>GS</sub> =-10V, R <sub>G</sub> =6Ω I <sub>D</sub> =-1A	---	23	40	ns
T <sub>r</sub>	Rise Time <sup>3, 4</sup>		---	10	20	
T <sub>d(off)</sub>	Turn-Off Delay Time <sup>3, 4</sup>		---	135	250	
T <sub>f</sub>	Fall Time <sup>3, 4</sup>		---	46	90	
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> =-25V, V <sub>GS</sub> =0V, F=1MHz	---	2757	4000	pF
C <sub>oss</sub>	Output Capacitance		---	240	360	
C <sub>rss</sub>	Reverse Transfer Capacitance		---	137	200	

**Drain-Source Diode Characteristics and Maximum Ratings**

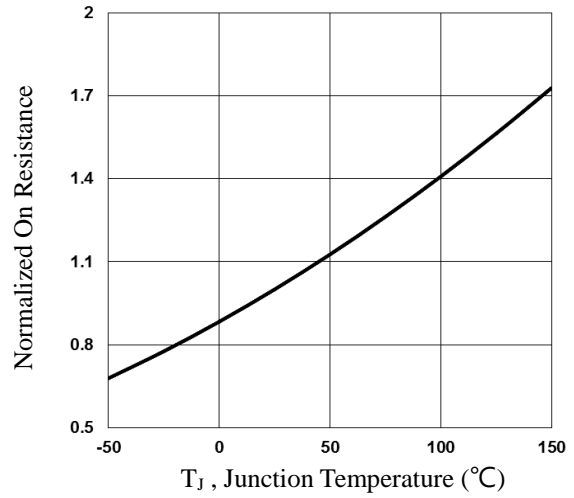
Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I <sub>S</sub>	Continuous Source Current	V <sub>G</sub> =V <sub>D</sub> =0V, Force Current	---	---	-38	A
I <sub>SM</sub>	Pulsed Source Current		---	---	-76	A
V <sub>SD</sub>	Diode Forward Voltage	V <sub>GS</sub> =0V, I <sub>S</sub> =-1A, T <sub>J</sub> =25°C	---	---	-1	V

Note :

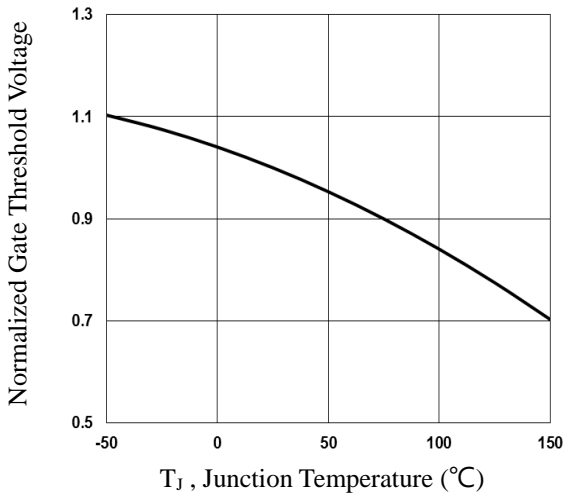
1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2. V<sub>DD</sub>=25V, V<sub>GS</sub>=10V, L=0.1mH, I<sub>AS</sub>=51A., R<sub>G</sub>=25Ω, Starting T<sub>J</sub>=25°C.
3. The data tested by pulsed, pulse width ≤ 300us, duty cycle ≤ 2%.
4. Essentially independent of operating temperature.



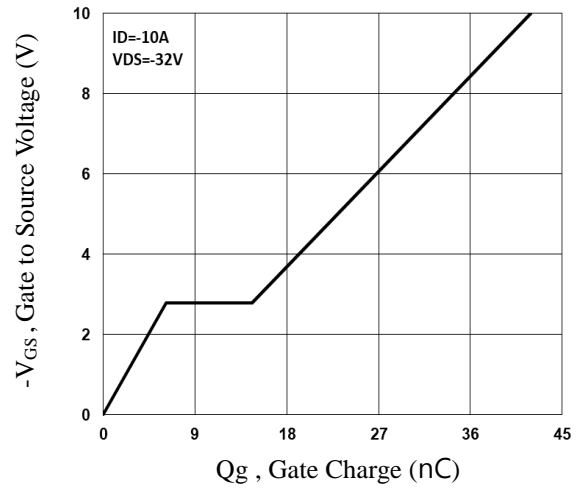
**Fig.1 Continuous Drain Current vs. T<sub>c</sub>**



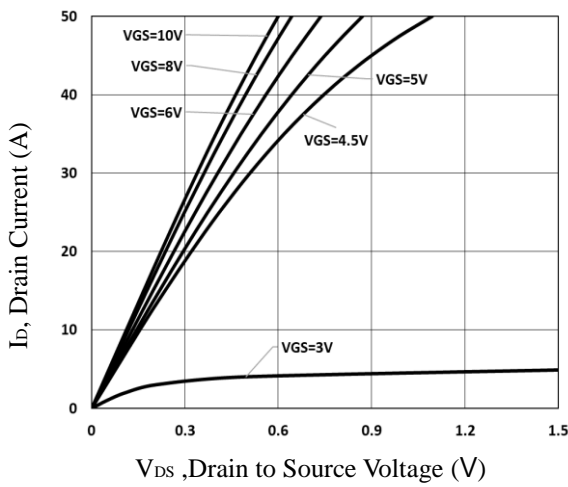
**Fig.2 Normalized R<sub>DS(on)</sub> vs. T<sub>j</sub>**



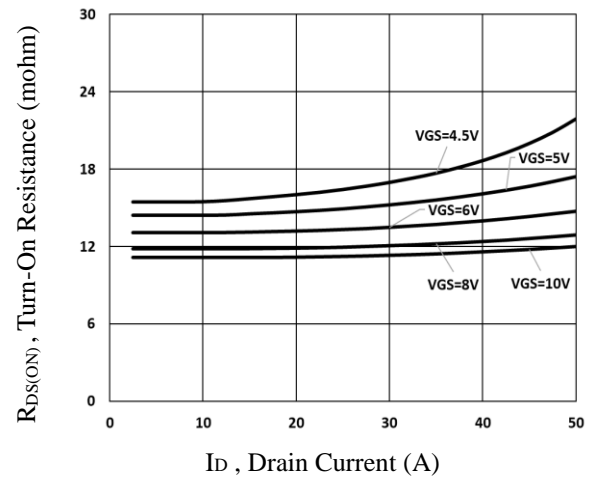
**Fig.3 Normalized V<sub>th</sub> vs. T<sub>j</sub>**



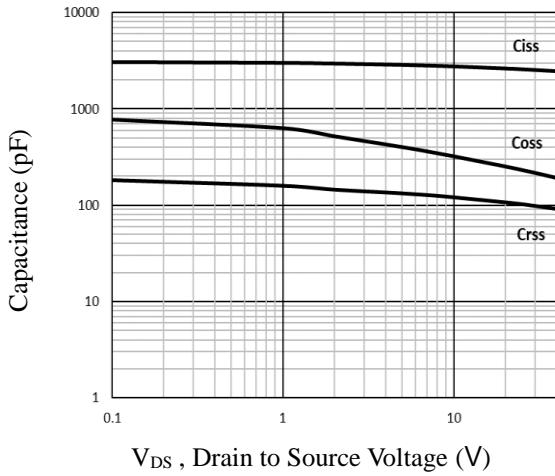
**Fig.4 Gate Charge Waveform**



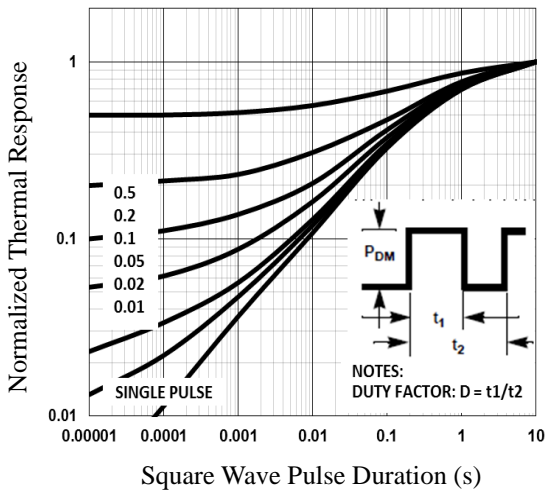
**Fig.5 Typical Output Characteristics**



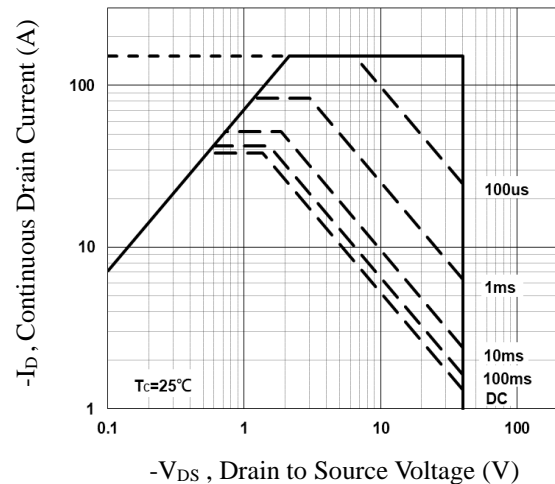
**Fig.6 Turn-On Resistance vs. I<sub>D</sub>**



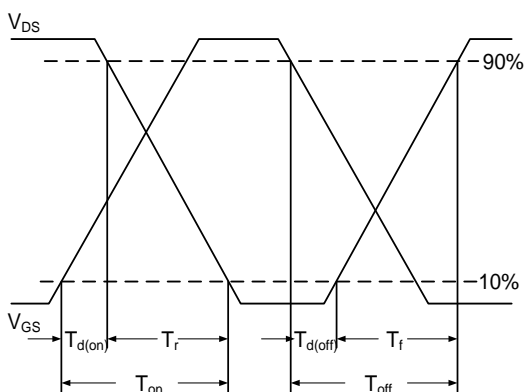
**Fig.7 Capacitance Characteristics**



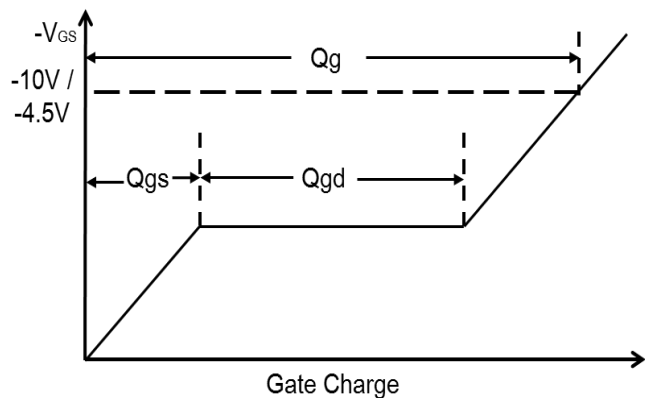
**Fig.8 Normalized Transient Impedance**



**Fig.9 Maximum Safe Operation Area**

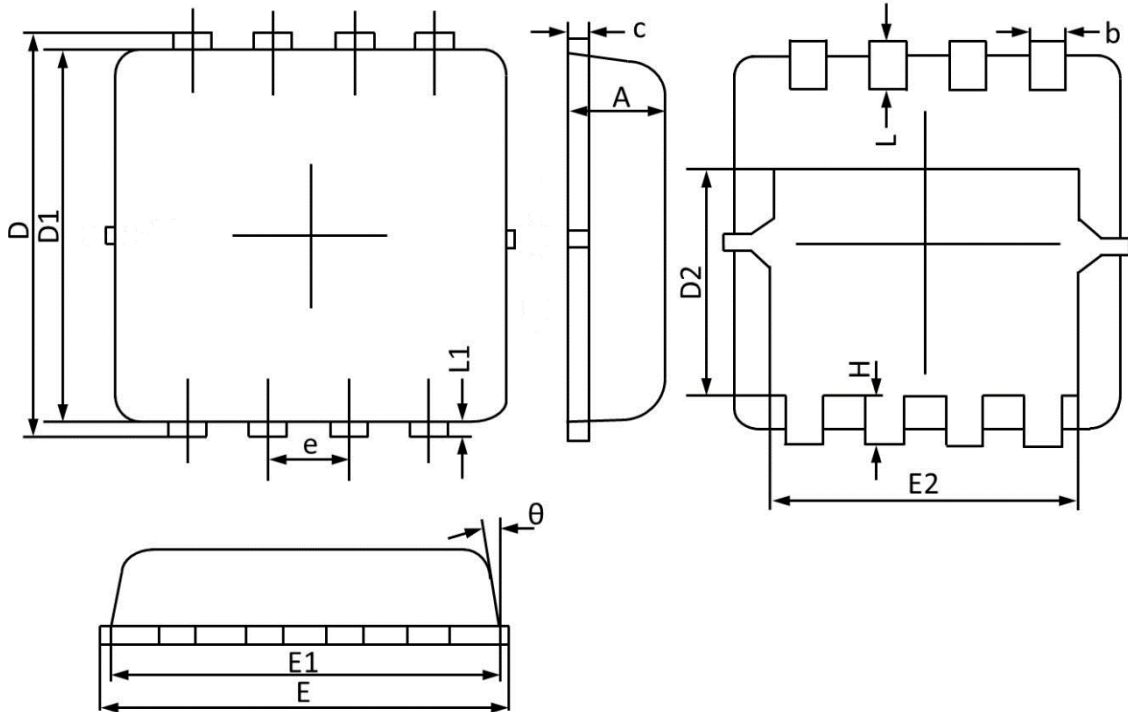


**Fig.10 Switching Time Waveform**



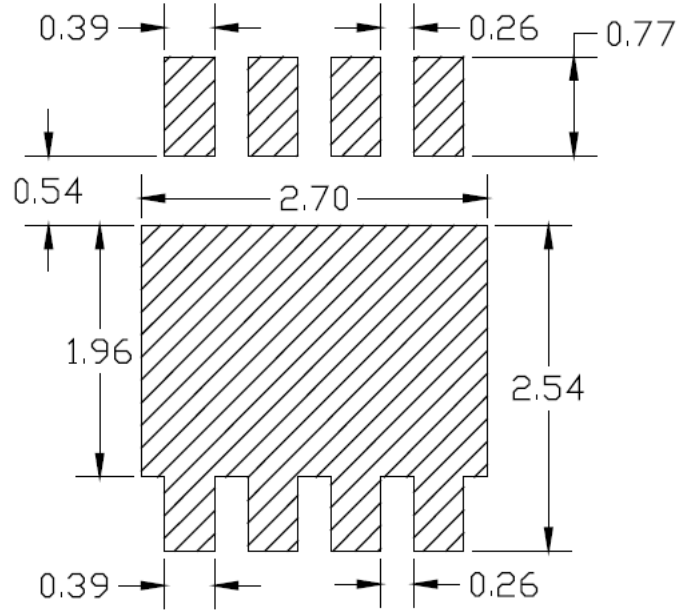
**Fig.11 Gate Charge Waveform**

**PPAK3x3 PACKAGE INFORMATION**



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	MAX	MIN	MAX	MIN
A	0.900	0.700	0.035	0.028
b	0.350	0.250	0.014	0.010
c	0.250	0.100	0.010	0.004
D	3.500	3.050	0.138	0.120
D1	3.200	2.900	0.126	0.114
D2	1.950	1.350	0.077	0.053
E	3.400	3.000	0.134	0.118
E1	3.300	2.900	0.130	0.114
E2	2.600	2.350	0.102	0.093
e	0.65BSC		0.026BSC	
H	0.750	0.300	0.030	0.012
L	0.600	0.300	0.024	0.012
L1	0.200	0.060	0.008	0.002
θ	14°	6°	14°	6°

### PPAK3X3 RECOMMENDED LAND PATTERN



unit : mm